FAZIA Status Meeting

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Board test - MCU(PIC) main 함수

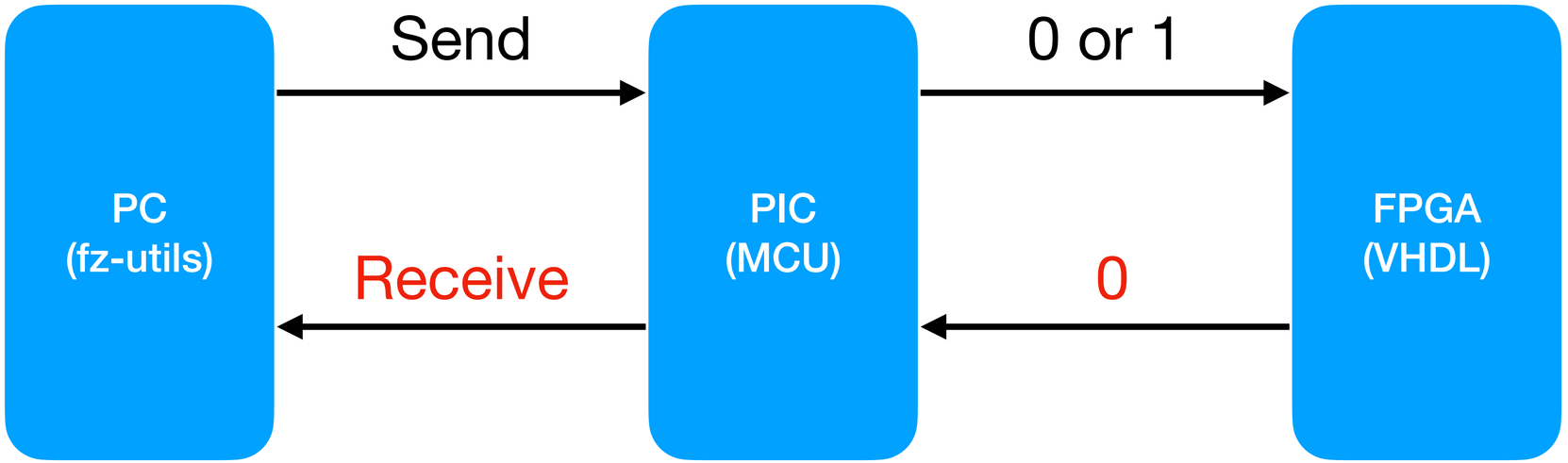
Initialize

```
while (!both_fpga_ok)
{
    get_data_fpga1 = (UINT) (getid() << 1); -> 0

    wrspi(1, 0x02, get_data_fpga1);         -> 0
    wrspi(2, 0x02, get_data_fpga1 + 0x01);  -> 1

    rd_data_fpga1 = rdspi(1, 0x02);
    rd_data_fpga2 = rdspi(2, 0x02);

    if ((rd_data_fpga1 == get_data_fpga1) && (rd_data_fpga2 == get_data_fpga1 + 0x01)) {
        both_fpga_ok = TRUE;
    }
} // end while
```

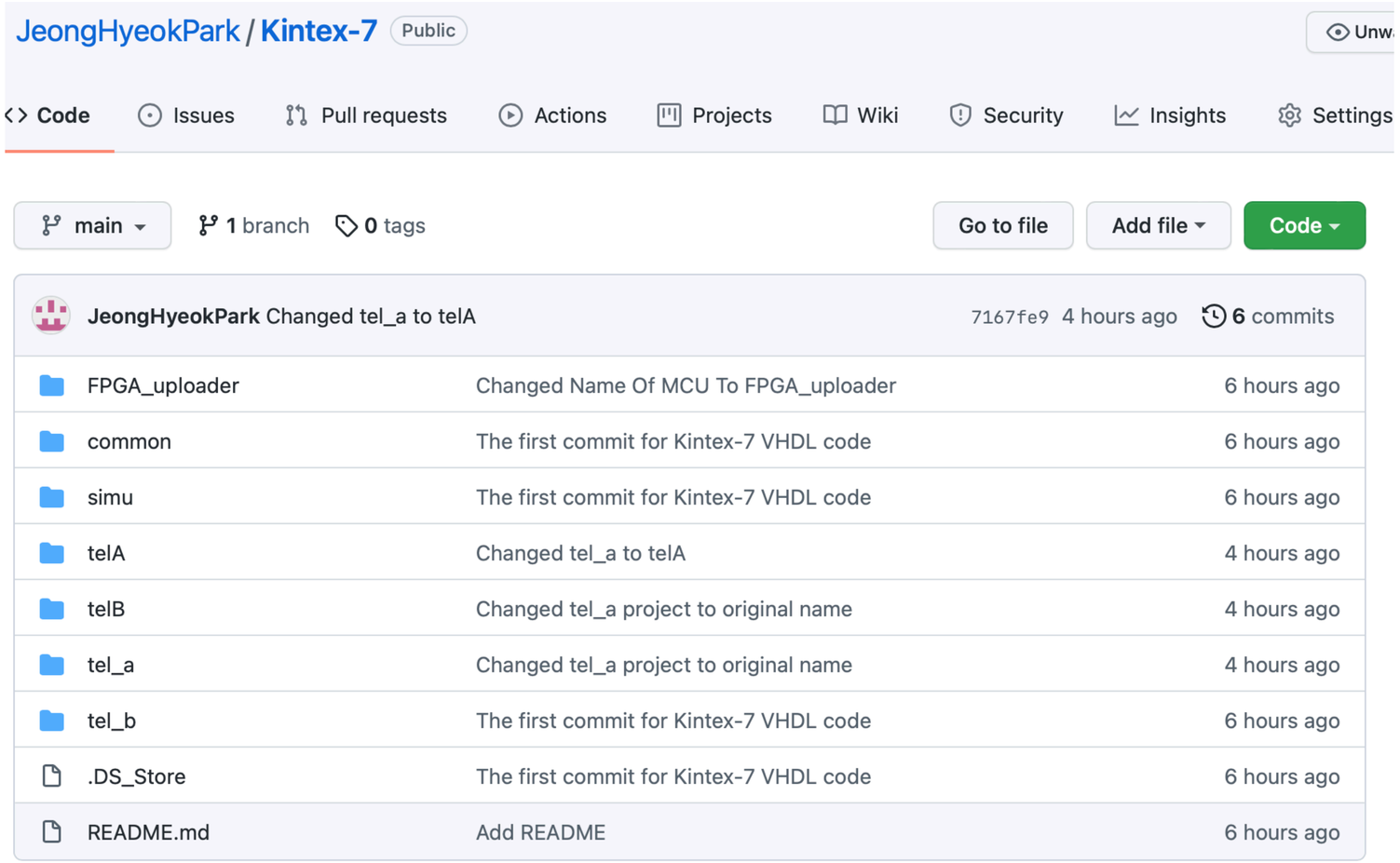
Function

1. FPGA (A,B)에 0,1 신호를 보낸다.
2. FPGA (A,B)에서 신호를 읽는다.
3. 두 FPGA에서 보낸 신호와 읽은 신호가 같으면 다음 작업을 실행한다.

Board test flow



Board test - GitHub



https://github.com/JeongHyeokPark/Kintex-7

BACKUPS