

# FAZIA Status Meeting

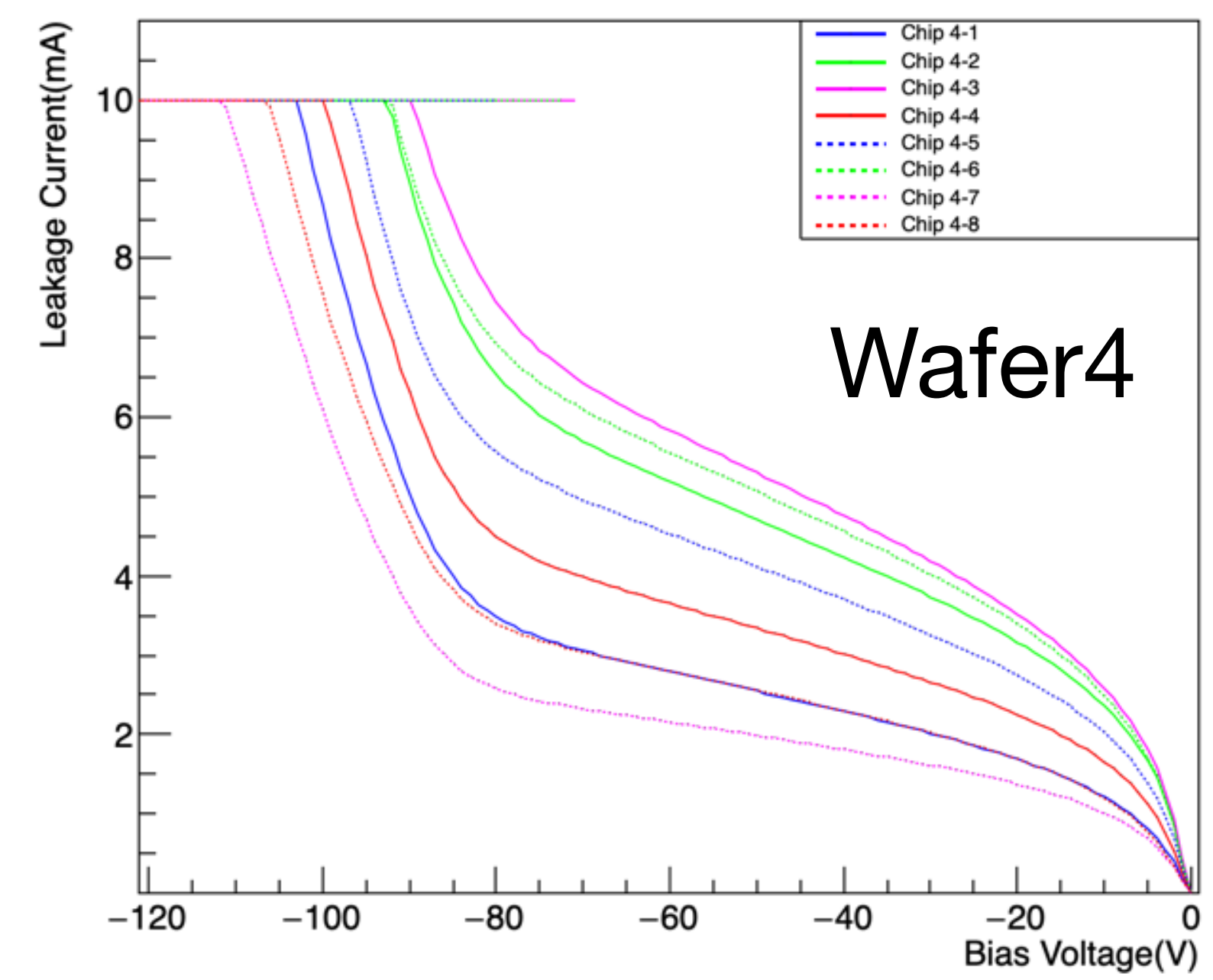
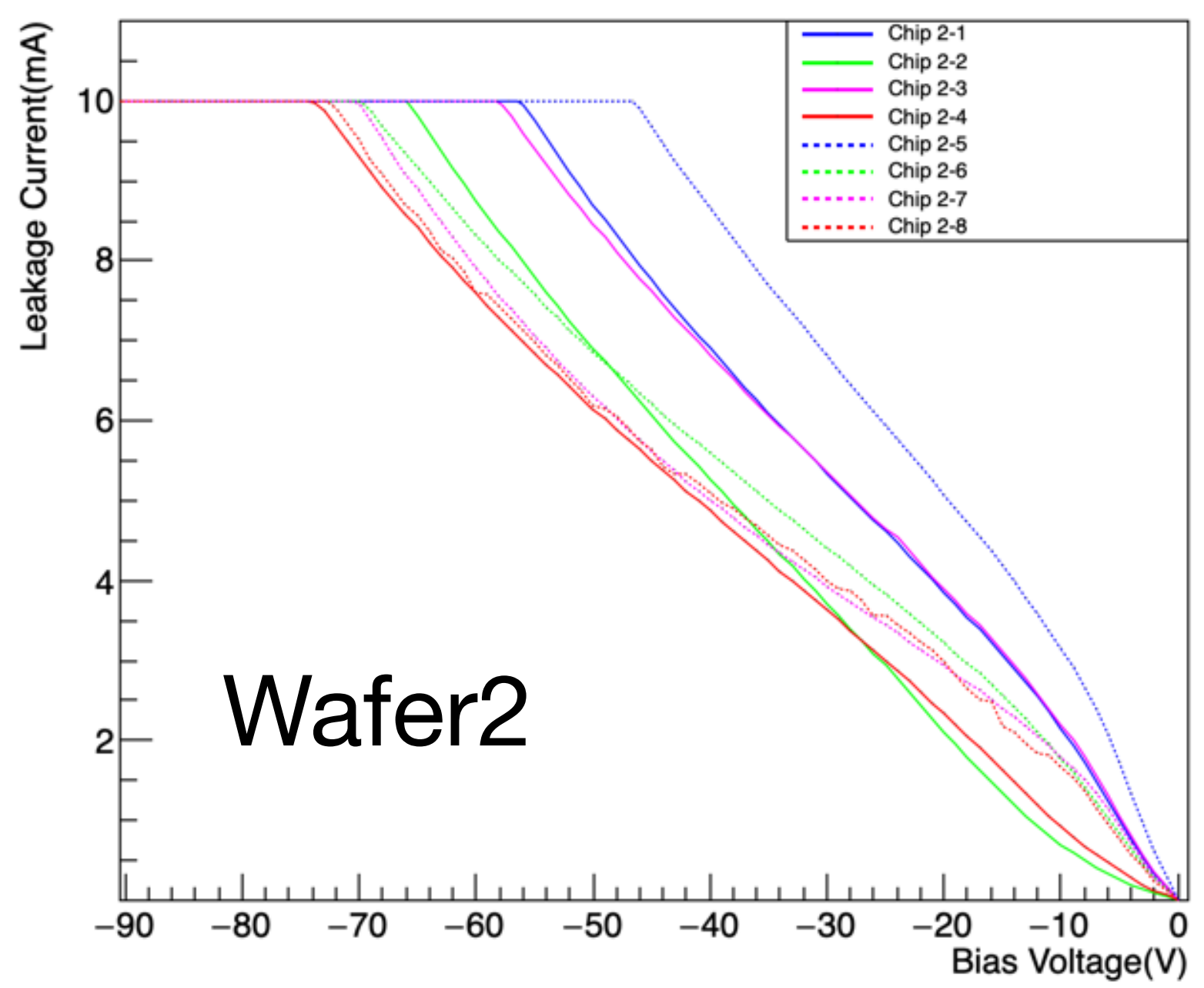
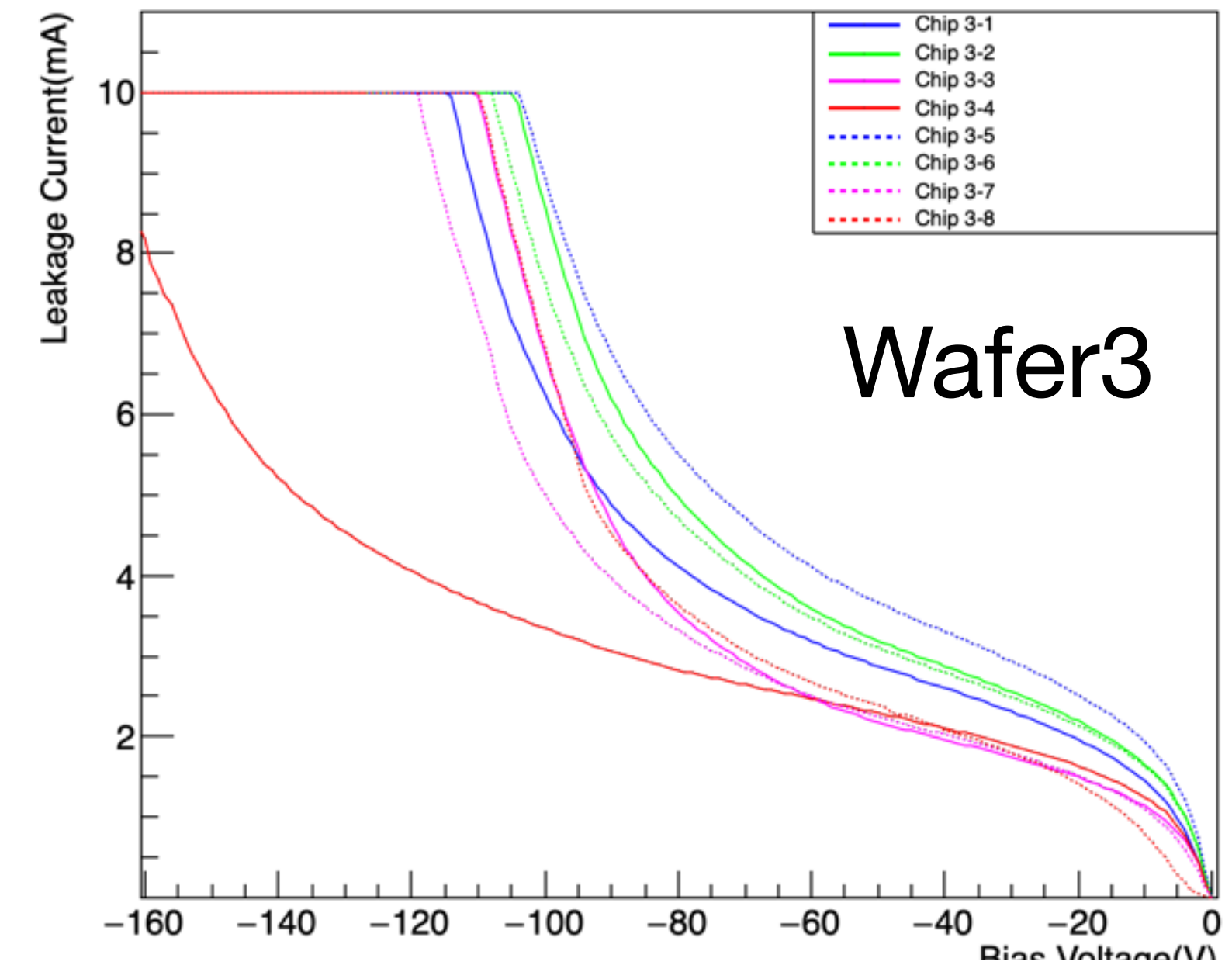
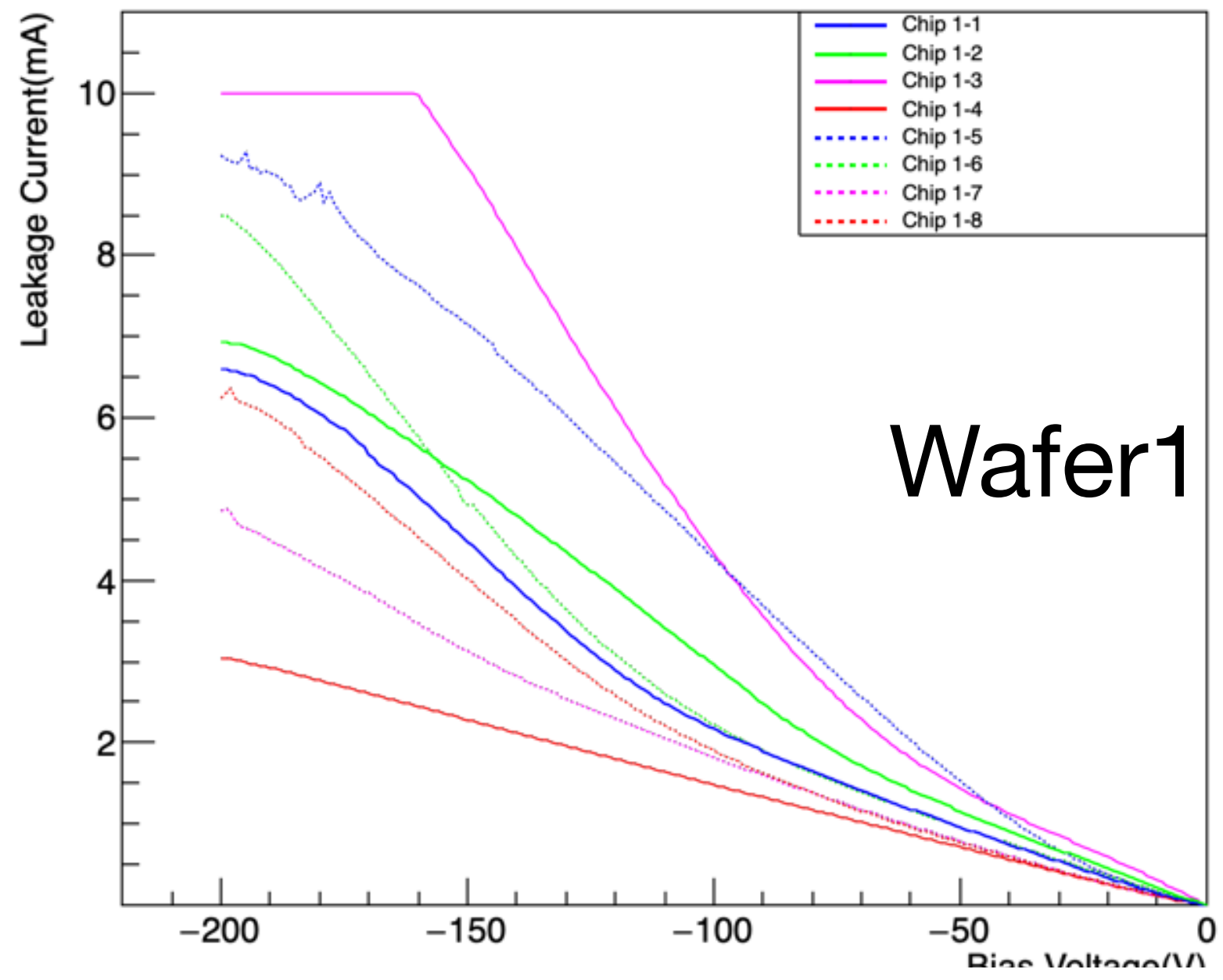
-Jeonghyeok Park-

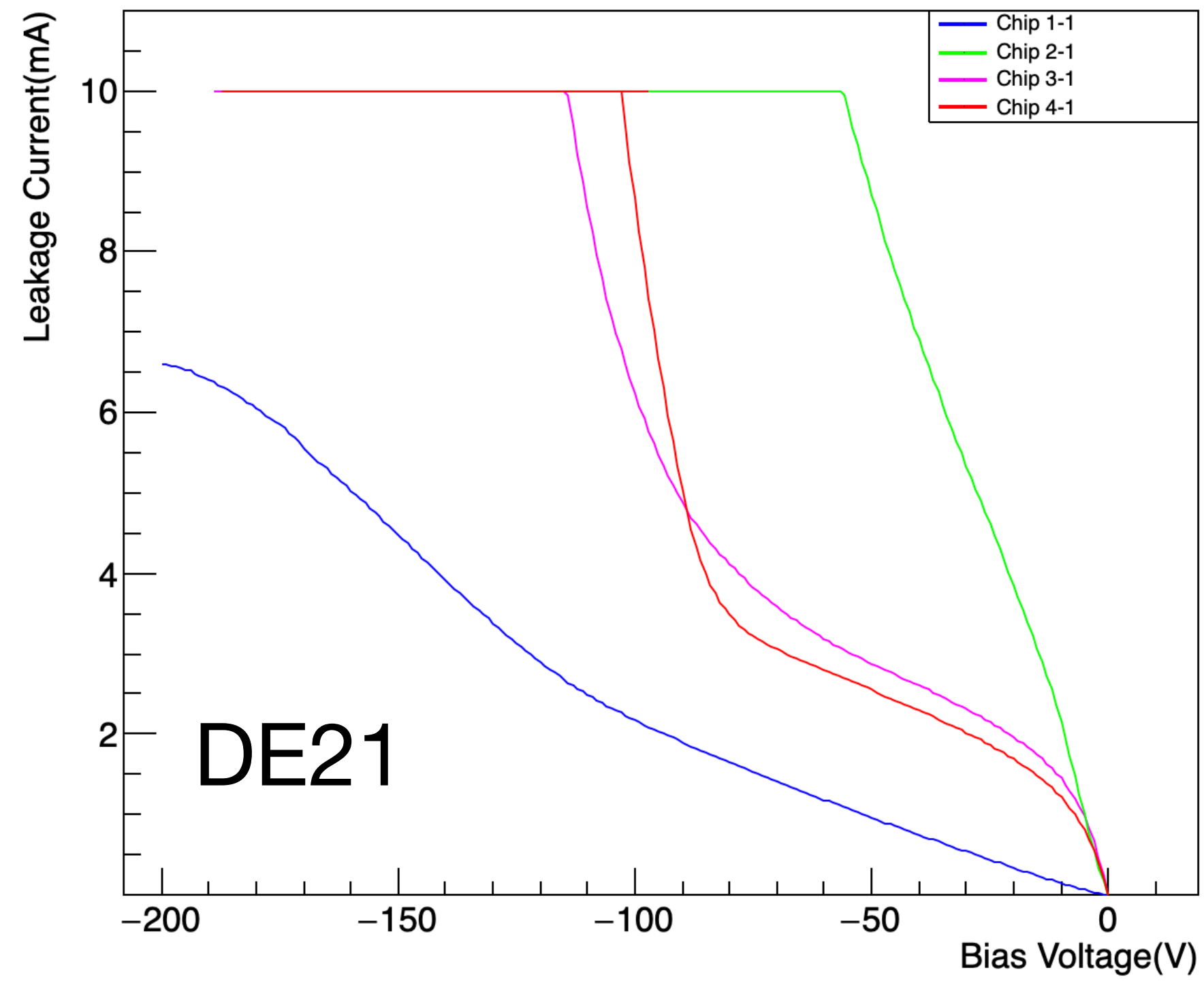
# 1. Chip test - DE21



32 Chips, (8 chips per wafer)

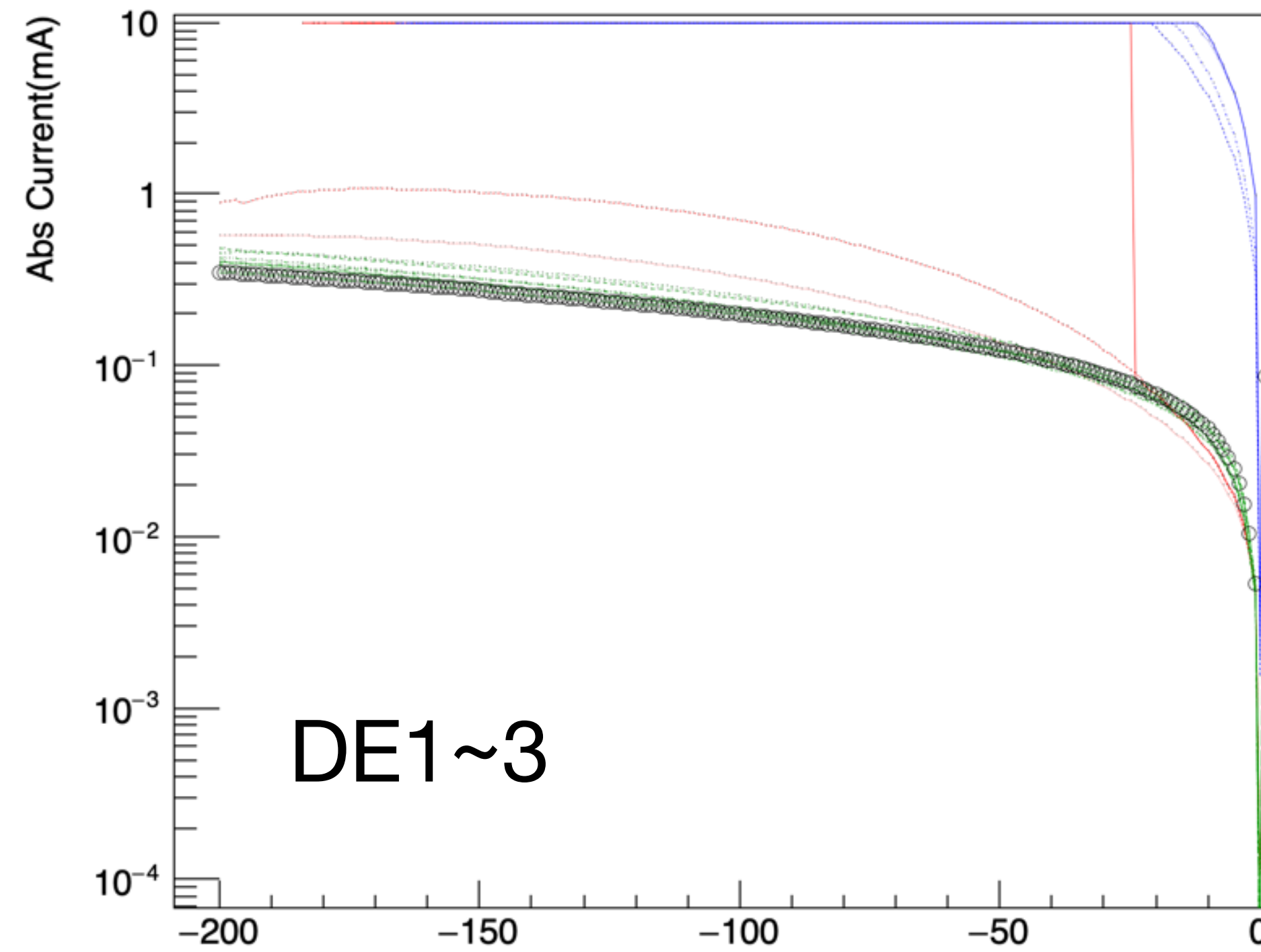
11월 17일 최경근 센터장님께 칩을 받음.





이번 측정

$\times \sim 10$



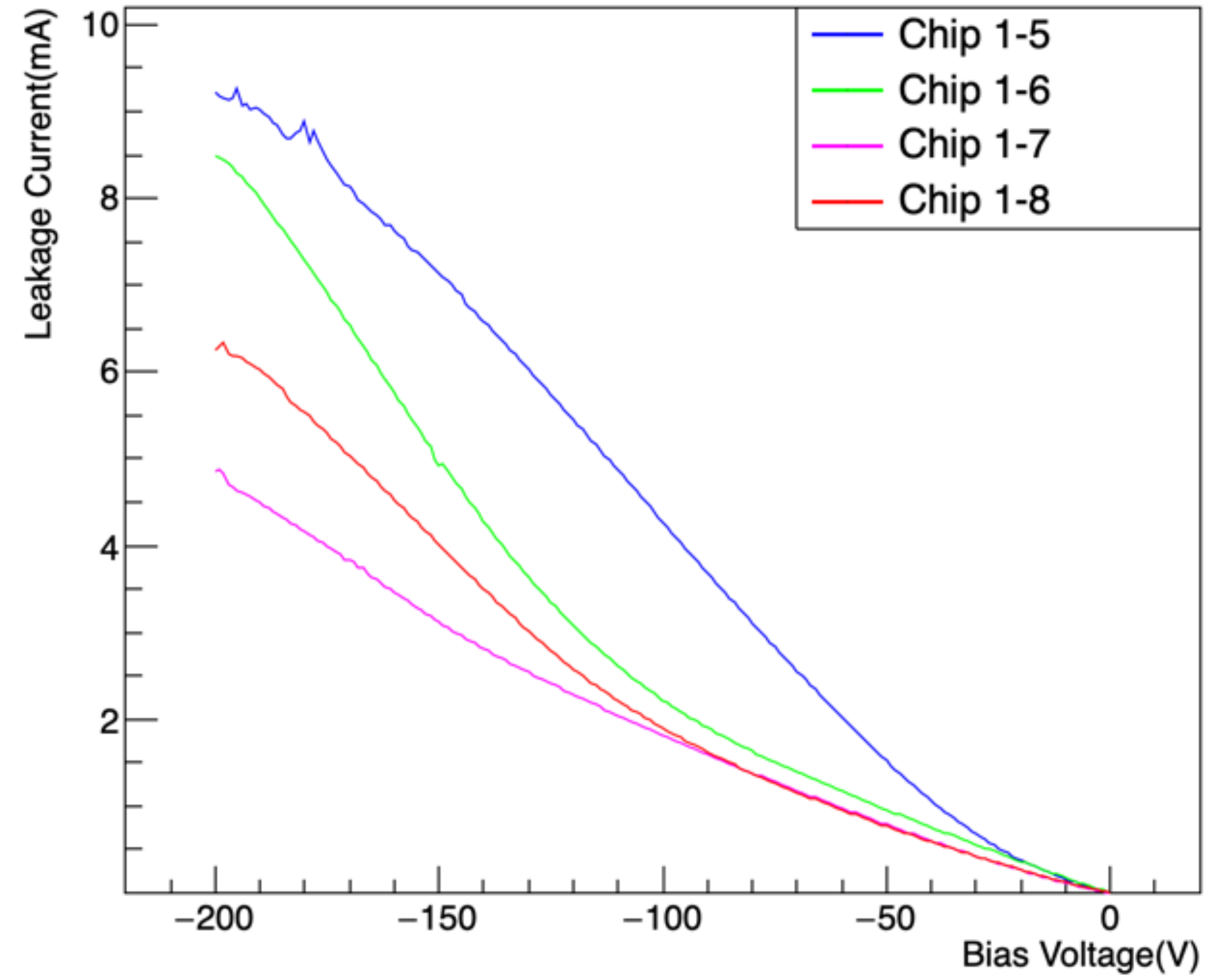
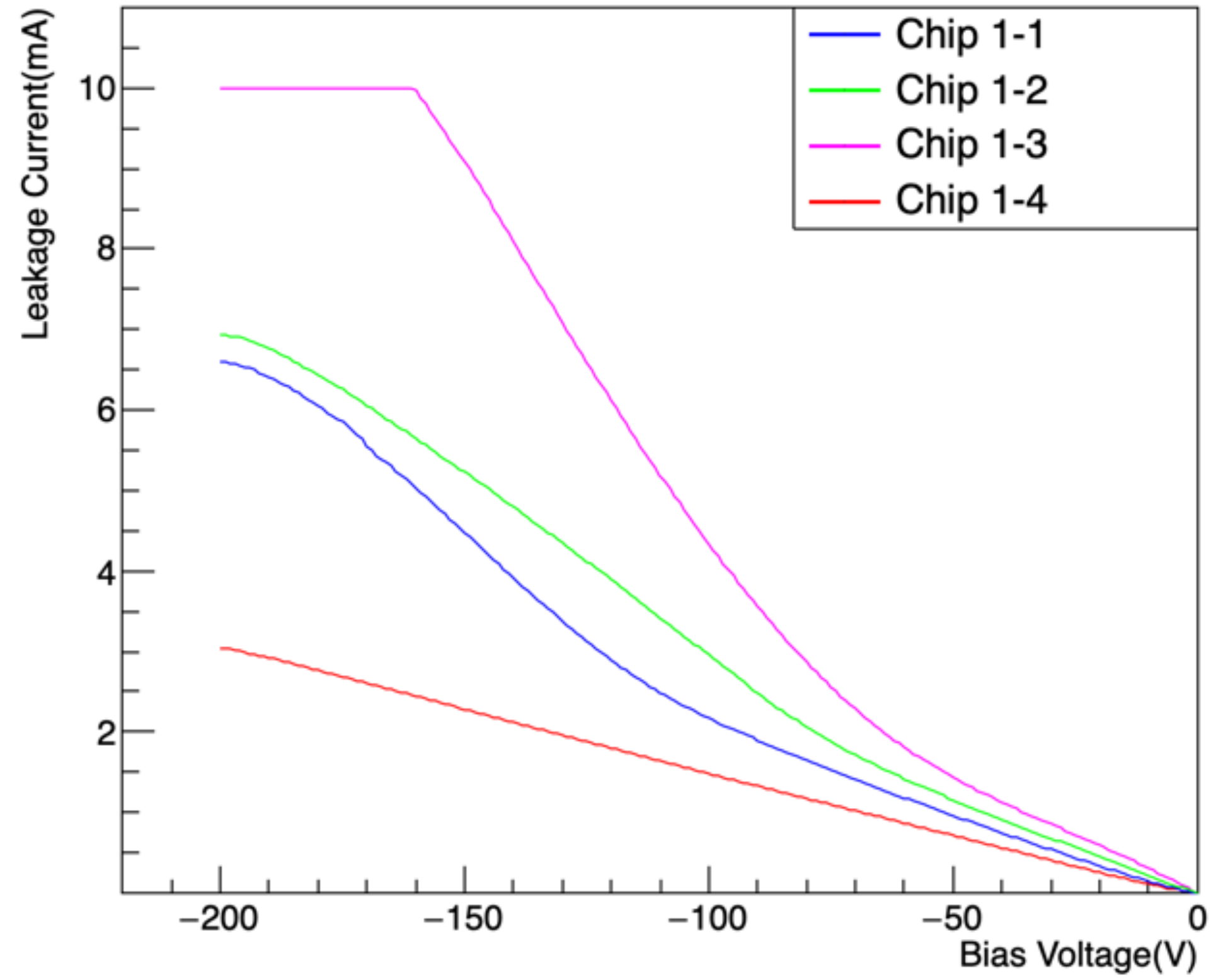
이전 측정

## Future plans

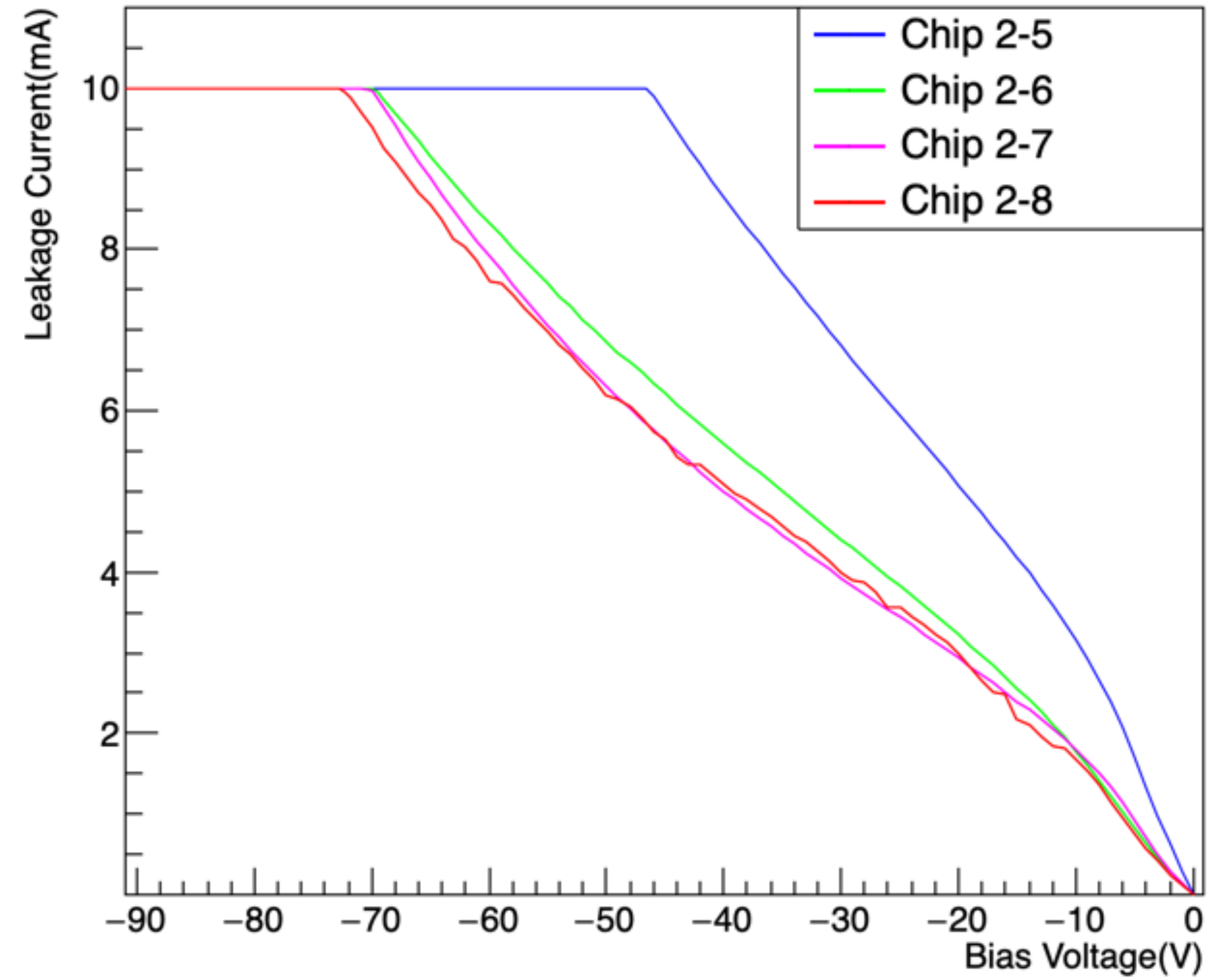
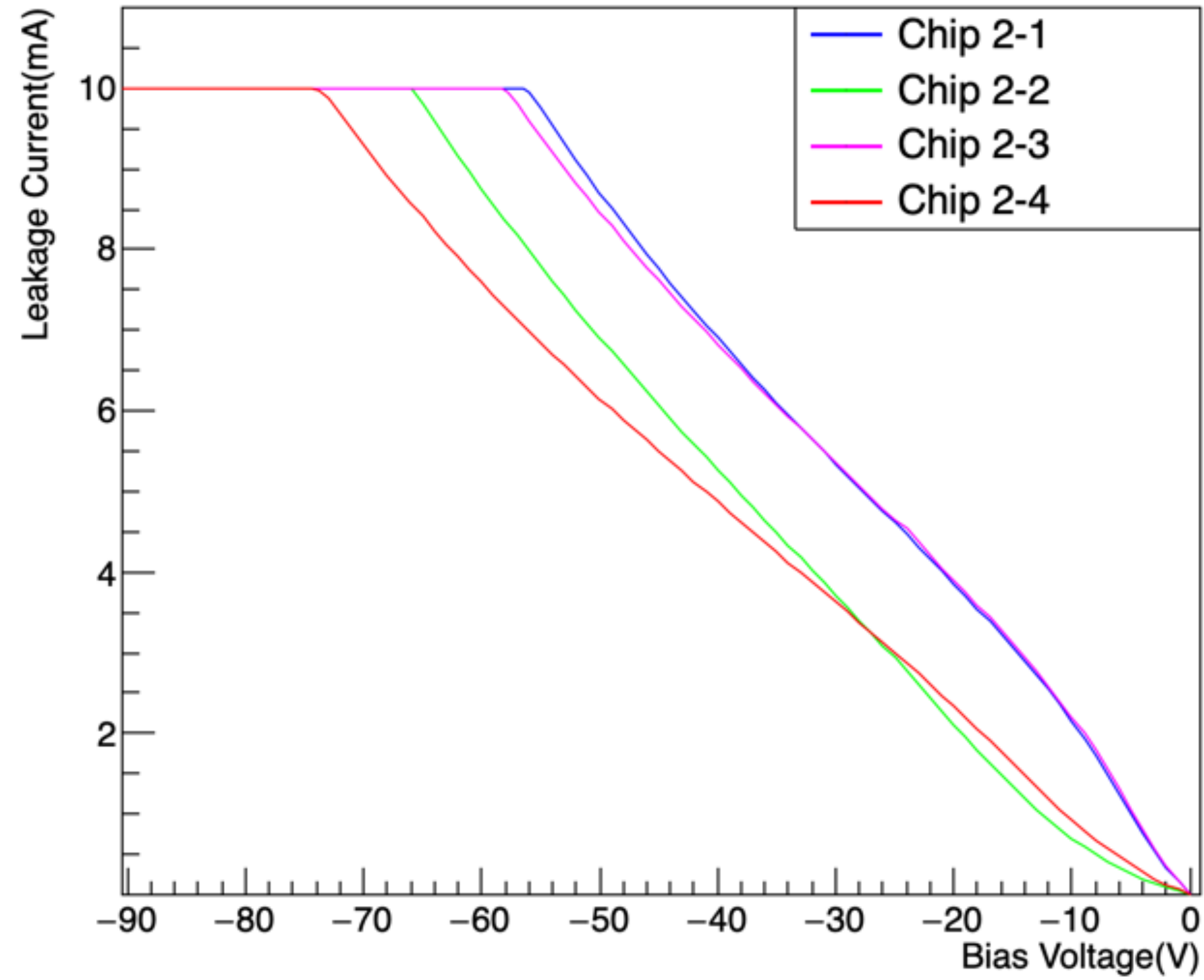
1. 노티스 방문(11월 25일, 목) - Board test용 firmware(MCU) 디버깅
  2. Probe station (11월 26일, 금) - 고려대 검수 / 인하대 설치
- + ?

# BACKUPS

# Chip test - DE21 (wafer1)

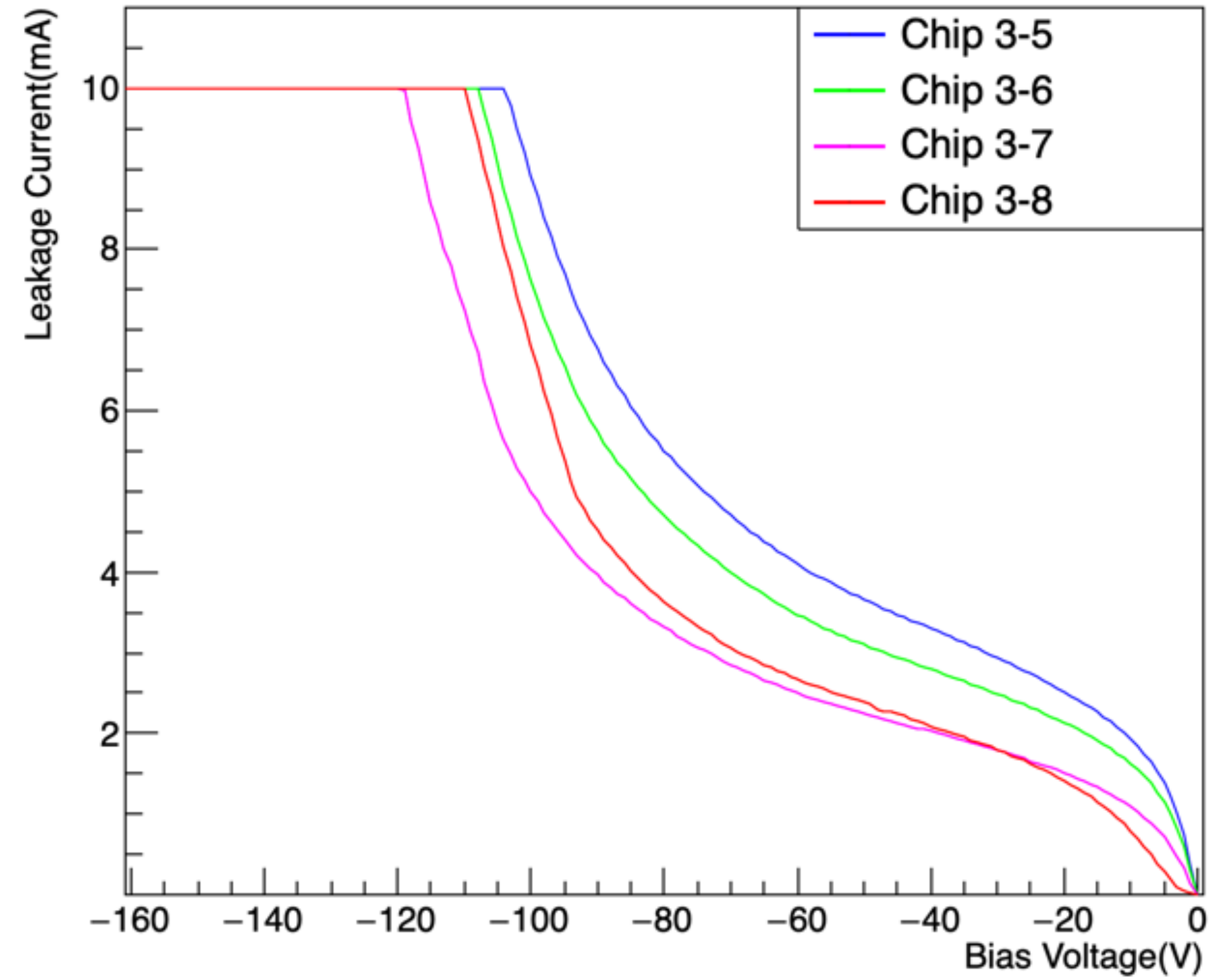
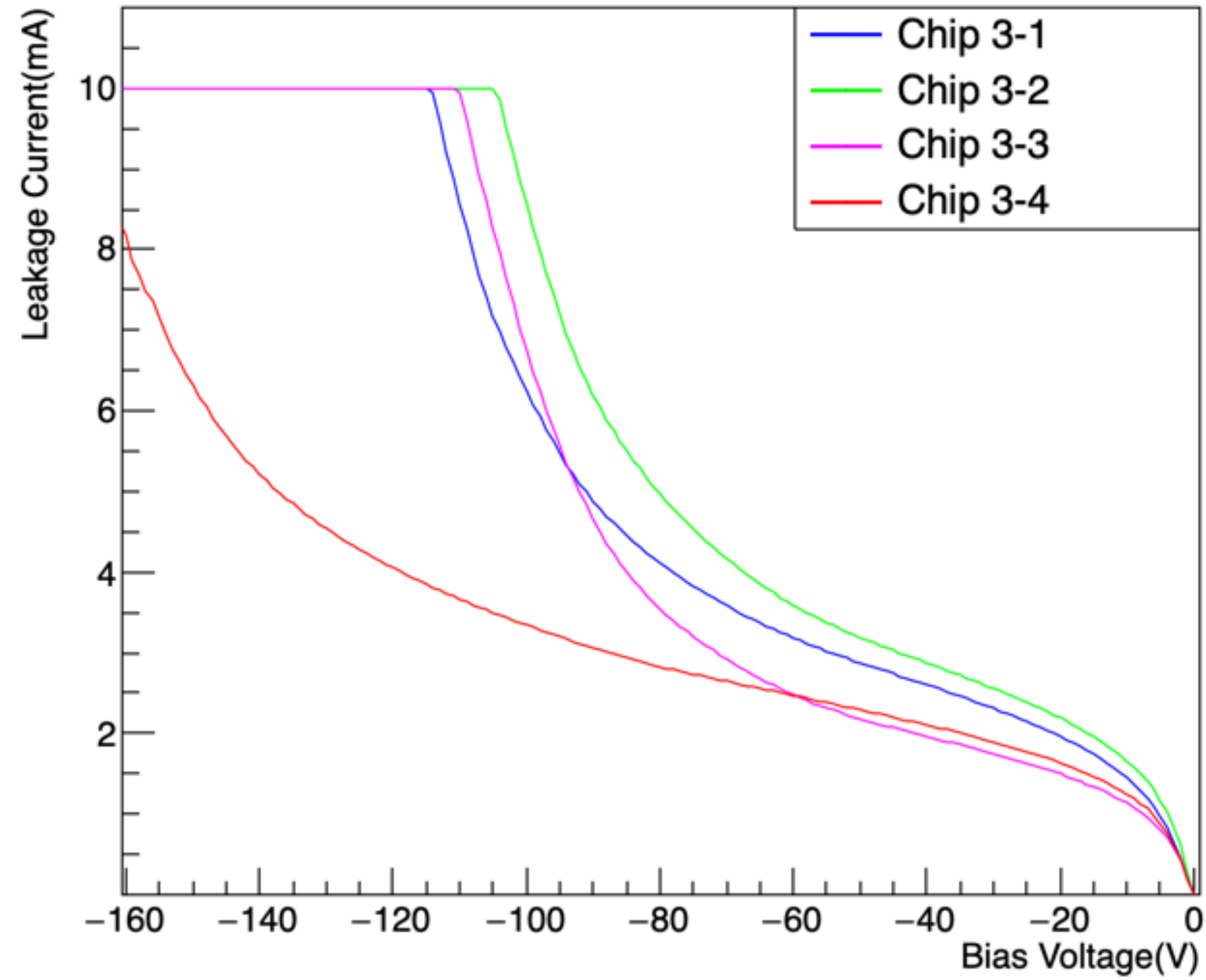


# Chip test - DE21 (wafer2)





# Chip test - DE21 (wafer3)



# Chip test - DE21 (wafer4)

